



Expertise, variety and quality



www.atrinelec.com

02166766957 - 02166766927



تهران پاسار امجد طبقه 1 واحد 16



info@atrinelec.com

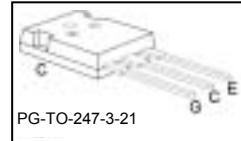
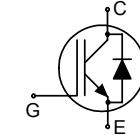


@atrinelec



**Low Loss DuoPack : IGBT in Trench and Fieldstop technology
with soft, fast recovery anti-parallel EmCon HE diode**

- Very low $V_{CE(sat)}$ 1.5 V (typ.)
- Maximum Junction Temperature 175 °C
- Short circuit withstand time – 5μs
- Positive temperature coefficient in $V_{CE(sat)}$
- very tight parameter distribution
- high ruggedness, temperature stable behaviour
- very high switching speed
- Low EMI
- Very soft, fast recovery anti-parallel EmCon HE diode
- Qualified according to JEDEC¹⁾ for target applications
- Pb-free lead plating; RoHS compliant
- Complete product spectrum and PSpice Models : <http://www.infineon.com/igbt/>



Applications:

- Frequency Converters
- Uninterrupted Power Supply

Type	V_{CE}	I_C	$V_{CE(sat), T_j=25^\circ C}$	$T_{j,max}$	Marking	Package
IKW75N60T	600V	75A	1.5V	175°C	K75T60	PG-TO-247-3-21

Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage	V_{CE}	600	V
DC collector current, limited by $T_{j,max}$	I_C	80 ²⁾	A
$T_C = 25^\circ C$		75	
$T_C = 100^\circ C$			
Pulsed collector current, t_p limited by $T_{j,max}$	I_{Cpuls}	225	
Turn off safe operating area ($V_{CE} \leq 600V$, $T_j \leq 175^\circ C$)	-	225	
Diode forward current, limited by $T_{j,max}$	I_F	80 ²⁾	
$T_C = 25^\circ C$		75	
$T_C = 100^\circ C$			
Diode pulsed current, t_p limited by $T_{j,max}$	I_{Fpuls}	225	
Gate-emitter voltage	V_{GE}	±20	V
Short circuit withstand time ³⁾	t_{SC}	5	μs
$V_{GE} = 15V$, $V_{CC} \leq 400V$, $T_j \leq 150^\circ C$			
Power dissipation $T_C = 25^\circ C$	P_{tot}	428	W
Operating junction temperature	T_j	-40...+175	°C
Storage temperature	T_{stg}	-55...+175	
Soldering temperature, 1.6mm (0.063 in.) from case for 10s	-	260	

¹⁾ J-STD-020 and JESD-022

²⁾ Value limited by bondwire

³⁾ Allowed number of short circuits: <1000; time between short circuits: >1s.

Thermal Resistance

Parameter	Symbol	Conditions	Max. Value	Unit
Characteristic				
IGBT thermal resistance, junction – case	R_{thJC}		0.35	K/W
Diode thermal resistance, junction – case	R_{thJCD}		0.6	
Thermal resistance, junction – ambient	R_{thJA}		40	

Electrical Characteristic, at $T_j = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	Typ.	max.	
Static Characteristic						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{GE}=0\text{V}, I_C=0.2\text{mA}$	600	-	-	V
Collector-emitter saturation voltage	$V_{CE(\text{sat})}$	$V_{GE} = 15\text{V}, I_C=75\text{A}$ $T_j=25^\circ\text{C}$ $T_j=175^\circ\text{C}$	-	1.5	2.0	
Diode forward voltage	V_F	$V_{GE}=0\text{V}, I_F=75\text{A}$ $T_j=25^\circ\text{C}$ $T_j=175^\circ\text{C}$	-	1.65	2.0	
Gate-emitter threshold voltage	$V_{GE(\text{th})}$	$I_C=1.2\text{mA}, V_{CE}=V_{GE}$	4.1	4.9	5.7	
Zero gate voltage collector current	I_{CES}	$V_{CE}=600\text{V}, V_{GE}=0\text{V}$ $T_j=25^\circ\text{C}$ $T_j=175^\circ\text{C}$	-	-	40	μA
-			-	-	1000	
Gate-emitter leakage current	I_{GES}	$V_{CE}=0\text{V}, V_{GE}=20\text{V}$	-	-	100	nA
Transconductance	g_{fs}	$V_{CE}=20\text{V}, I_C=75\text{A}$	-	41	-	S
Integrated gate resistor	R_{Gint}			-		Ω

Dynamic Characteristic

Input capacitance	C_{iss}	$V_{CE}=25\text{V}, V_{GE}=0\text{V}, f=1\text{MHz}$	-	4620	-	pF
Output capacitance	C_{oss}		-	288	-	
Reverse transfer capacitance	C_{rss}		-	137	-	
Gate charge	Q_{Gate}	$V_{CC}=480\text{V}, I_C=75\text{A}$ $V_{GE}=15\text{V}$	-	470	-	nC
Internal emitter inductance measured 5mm (0.197 in.) from case	L_E		-	13	-	nH
Short circuit collector current ¹⁾	$I_{C(SC)}$	$V_{GE}=15\text{V}, t_{SC}\leq 5\mu\text{s}$ $V_{CC} = 400\text{V}, T_j \leq 150^\circ\text{C}$	-	687.5	-	A

¹⁾ Allowed number of short circuits: <1000; time between short circuits: >1s.

Switching Characteristic, Inductive Load, at $T_j=25^\circ\text{C}$

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	$T_j=25^\circ\text{C}$, $V_{CC}=400\text{V}$, $I_C=75\text{A}$, $V_{GE}=0/15\text{V}$, $R_G=5\Omega$, $L_\sigma^{(1)}=100\text{nH}$, $C_\sigma^{(1)}=39\text{pF}$ Energy losses include “tail” and diode reverse recovery.	-	33	-	ns
Rise time	t_r		-	36	-	
Turn-off delay time	$t_{d(off)}$		-	330	-	
Fall time	t_f		-	35	-	
Turn-on energy	E_{on}		-	2.0	-	mJ
Turn-off energy	E_{off}		-	2.5	-	
Total switching energy	E_{ts}		-	4.5	-	

Anti-Parallel Diode Characteristic

Diode reverse recovery time	t_{rr}	$T_j=25^\circ\text{C}$, $V_R=400\text{V}$, $I_F=75\text{A}$, $di_F/dt=1460\text{A}/\mu\text{s}$	-	121	-	ns
Diode reverse recovery charge	Q_{rr}		-	2.4	-	μC
Diode peak reverse recovery current	I_{rrm}		-	38.5	-	A
Diode peak rate of fall of reverse recovery current during t_b	di_{rr}/dt		-	921	-	$\text{A}/\mu\text{s}$

Switching Characteristic, Inductive Load, at $T_j=175^\circ\text{C}$

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	$T_j=175^\circ\text{C}$, $V_{CC}=400\text{V}$, $I_C=75\text{A}$, $V_{GE}=0/15\text{V}$, $R_G=5\Omega$, $L_\sigma^{(1)}=100\text{nH}$, $C_\sigma^{(1)}=39\text{pF}$ Energy losses include “tail” and diode reverse recovery.	-	32	-	ns
Rise time	t_r		-	37	-	
Turn-off delay time	$t_{d(off)}$		-	363	-	
Fall time	t_f		-	38	-	
Turn-on energy	E_{on}		-	2.9	-	mJ
Turn-off energy	E_{off}		-	2.9	-	
Total switching energy	E_{ts}		-	5.8	-	

Anti-Parallel Diode Characteristic

Diode reverse recovery time	t_{rr}	$T_j=175^\circ\text{C}$, $V_R=400\text{V}$, $I_F=75\text{A}$, $di_F/dt=1460\text{A}/\mu\text{s}$	-	182	-	ns
Diode reverse recovery charge	Q_{rr}		-	5.8	-	μC
Diode peak reverse recovery current	I_{rrm}		-	56.2	-	A
Diode peak rate of fall of reverse recovery current during t_b	di_{rr}/dt		-	1013	-	$\text{A}/\mu\text{s}$

¹⁾ Leakage inductance L_σ and Stray capacity C_σ due to dynamic test circuit in Figure E.

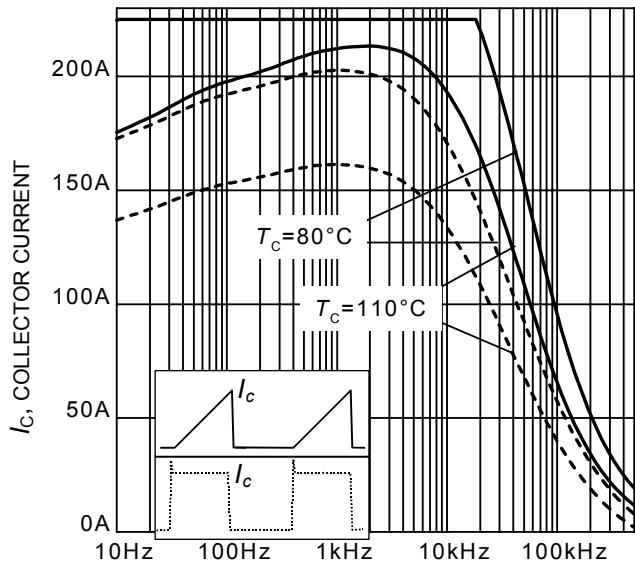

 f , SWITCHING FREQUENCY

Figure 1. Collector current as a function of switching frequency
 $(T_j \leq 175^\circ\text{C}, D = 0.5, V_{CE} = 400\text{V}, V_{GE} = 0/+15\text{V}, R_G = 5\Omega)$

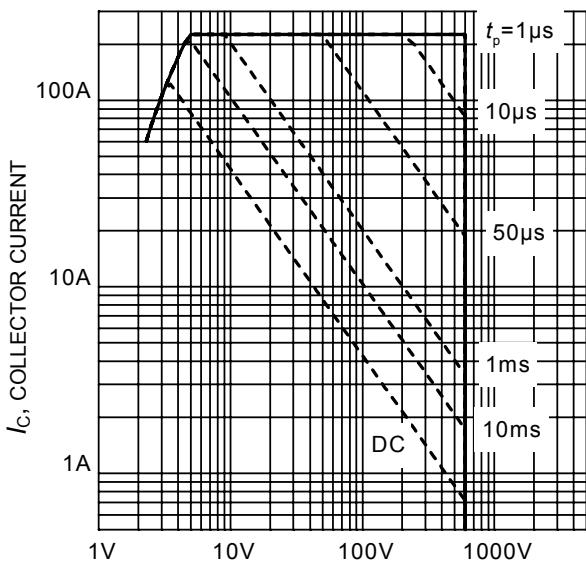

 V_{CE} , COLLECTOR-EMITTER VOLTAGE

Figure 2. Safe operating area
 $(D = 0, T_C = 25^\circ\text{C}, T_j \leq 175^\circ\text{C}, V_{GE}=15\text{V})$

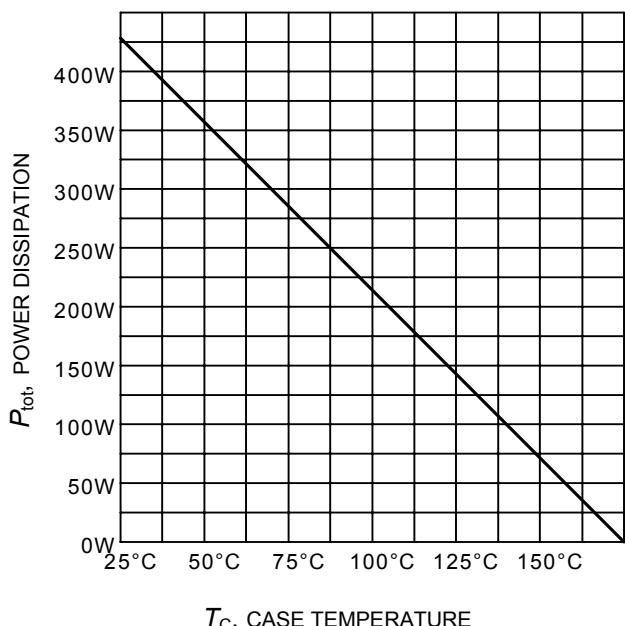

 T_C , CASE TEMPERATURE

Figure 3. Power dissipation as a function of case temperature
 $(T_j \leq 175^\circ\text{C})$

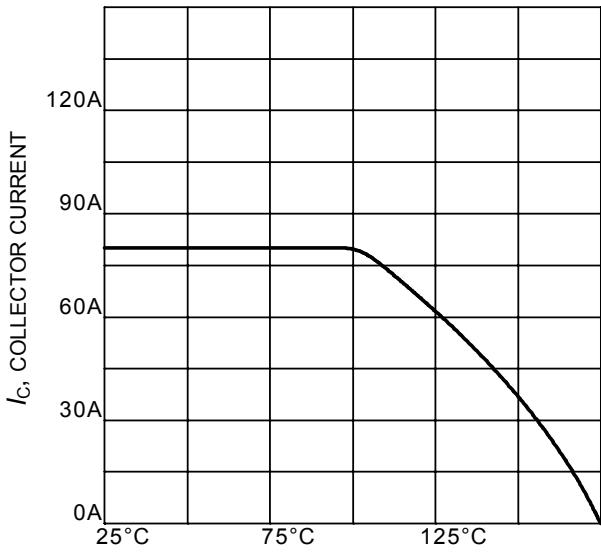

 T_C , CASE TEMPERATURE

Figure 4. DC Collector current as a function of case temperature
 $(V_{GE} \geq 15\text{V}, T_j \leq 175^\circ\text{C})$

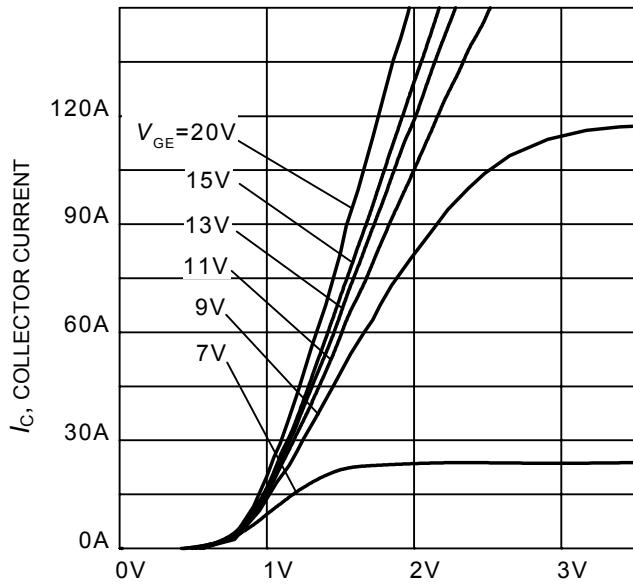
TrenchStop Series

 V_{CE} , COLLECTOR-EMITTER VOLTAGE

Figure 5. Typical output characteristic
($T_j = 25^\circ\text{C}$)

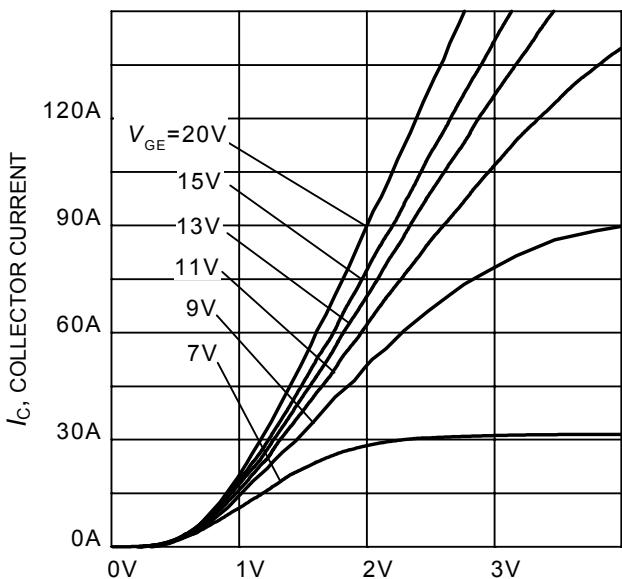

 V_{CE} , COLLECTOR-EMITTER VOLTAGE

Figure 6. Typical output characteristic
($T_j = 175^\circ\text{C}$)

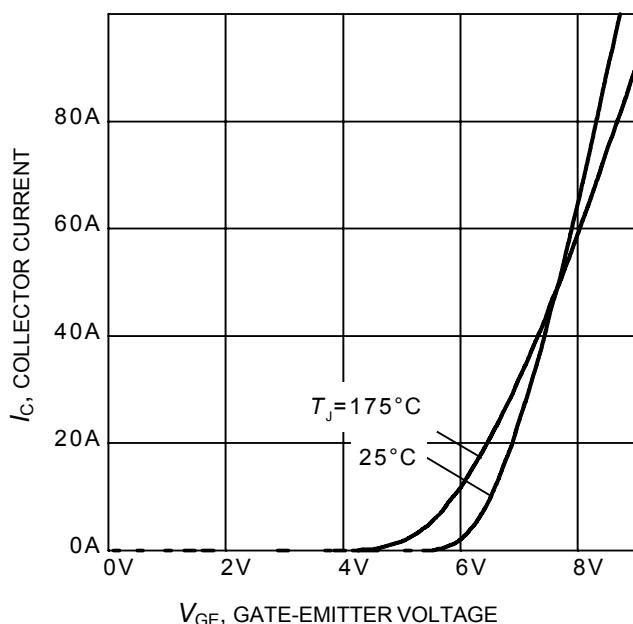

 V_{GE} , GATE-EMITTER VOLTAGE

Figure 7. Typical transfer characteristic
($V_{CE}=20\text{V}$)

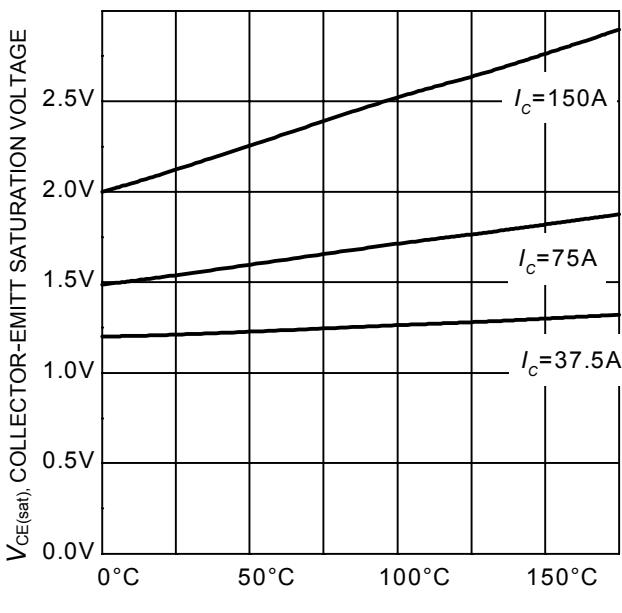

 T_j , JUNCTION TEMPERATURE

Figure 8. Typical collector-emitter saturation voltage as a function of junction temperature
($V_{GE} = 15\text{V}$)

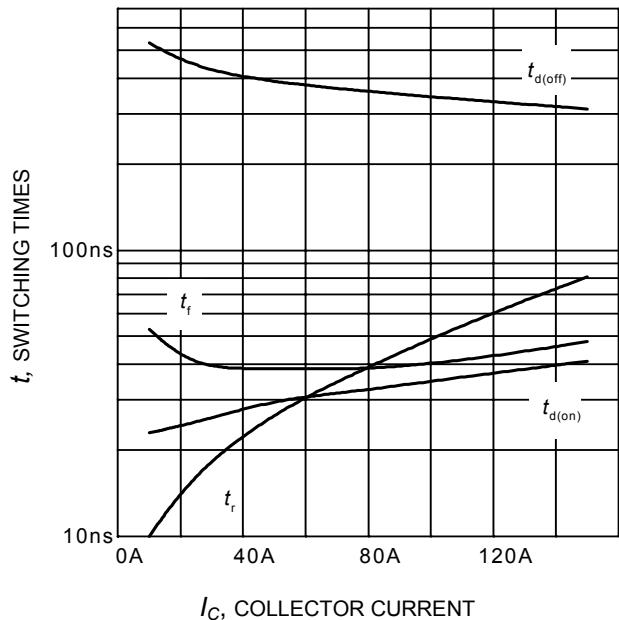

 I_C , COLLECTOR CURRENT

Figure 9. Typical switching times as a function of collector current
(inductive load, $T_J=175^\circ\text{C}$,
 $V_{CE} = 400\text{V}$, $V_{GE} = 0/15\text{V}$, $R_G = 5\Omega$,
Dynamic test circuit in Figure E)

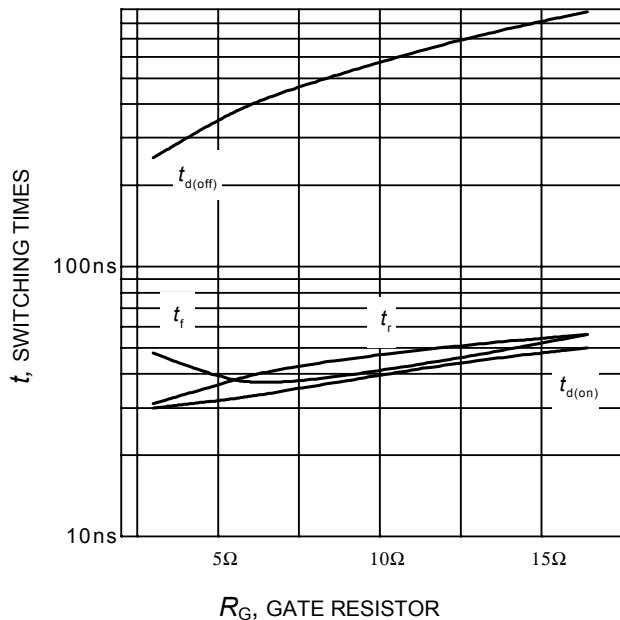

 R_G , GATE RESISTOR

Figure 10. Typical switching times as a function of gate resistor
(inductive load, $T_J = 175^\circ\text{C}$,
 $V_{CE} = 400\text{V}$, $V_{GE} = 0/15\text{V}$, $I_C = 75\text{A}$,
Dynamic test circuit in Figure E)

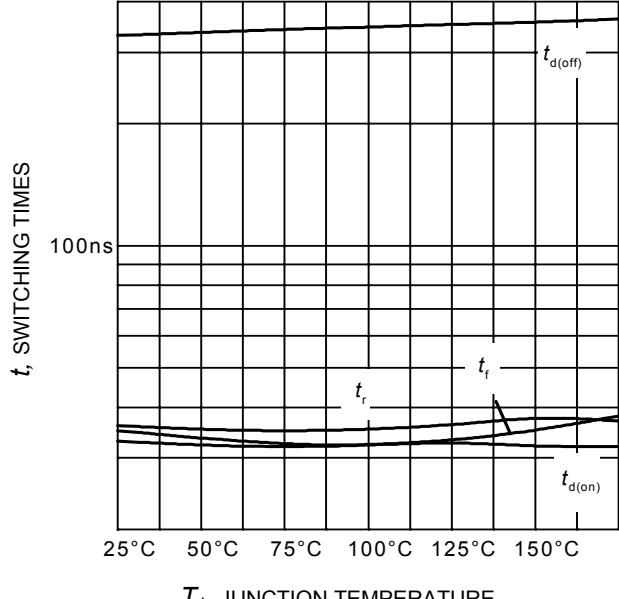

 T_J , JUNCTION TEMPERATURE

Figure 11. Typical switching times as a function of junction temperature
(inductive load, $V_{CE} = 400\text{V}$,
 $V_{GE} = 0/15\text{V}$, $I_C = 10\text{A}$, $R_G = 5\Omega$,
Dynamic test circuit in Figure E)

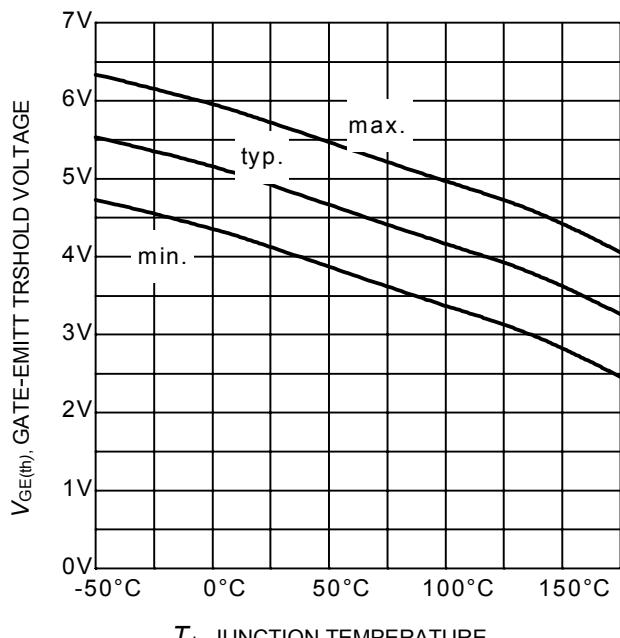

 T_J , JUNCTION TEMPERATURE

Figure 12. Gate-emitter threshold voltage as a function of junction temperature
($I_C = 1.2\text{mA}$)

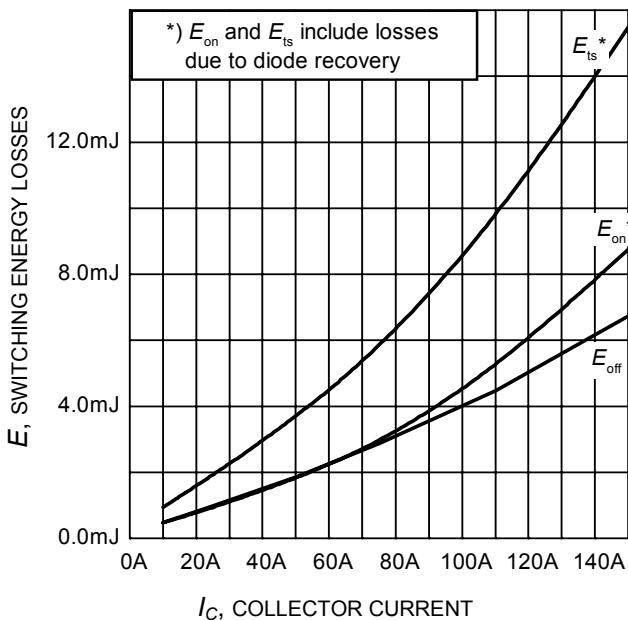


Figure 13. Typical switching energy losses as a function of collector current
(inductive load, $T_J = 175^\circ\text{C}$,
 $V_{CE} = 400\text{V}$, $V_{GE} = 0/15\text{V}$, $R_G = 5\Omega$,
Dynamic test circuit in Figure E)

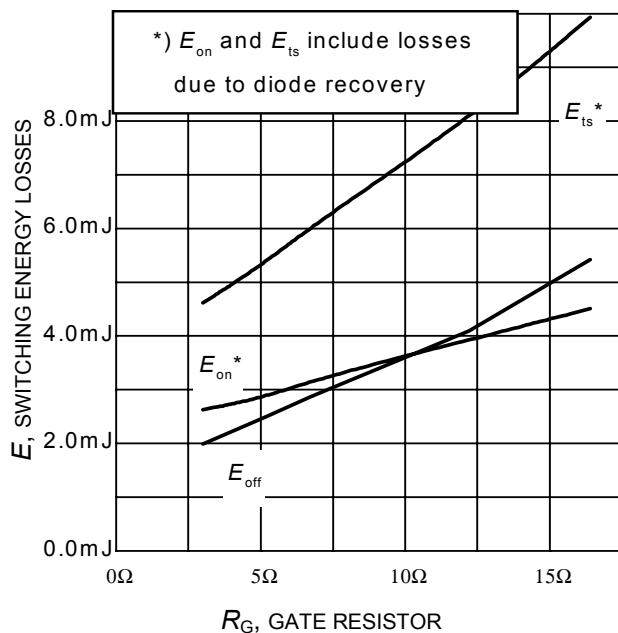


Figure 14. Typical switching energy losses as a function of gate resistor
(inductive load, $T_J = 175^\circ\text{C}$,
 $V_{CE} = 400\text{V}$, $V_{GE} = 0/15\text{V}$, $I_C = 75\text{A}$,
Dynamic test circuit in Figure E)

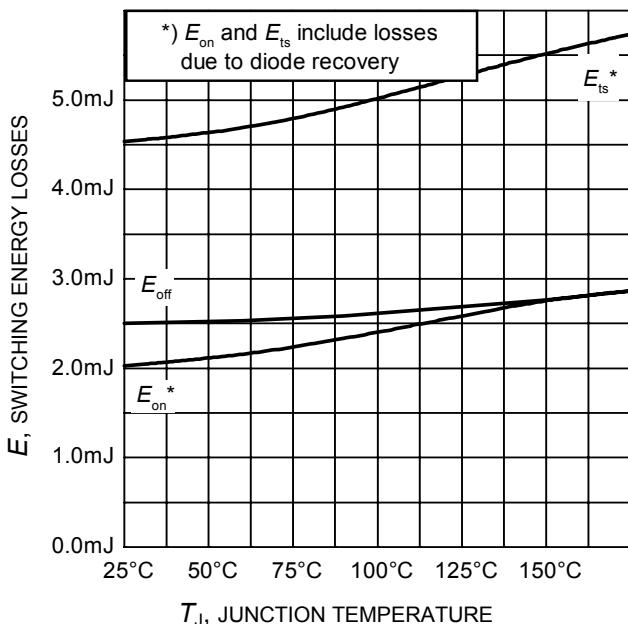


Figure 15. Typical switching energy losses as a function of junction temperature
(inductive load, $V_{CE} = 400\text{V}$,
 $V_{GE} = 0/15\text{V}$, $I_C = 75\text{A}$, $R_G = 5\Omega$,
Dynamic test circuit in Figure E)

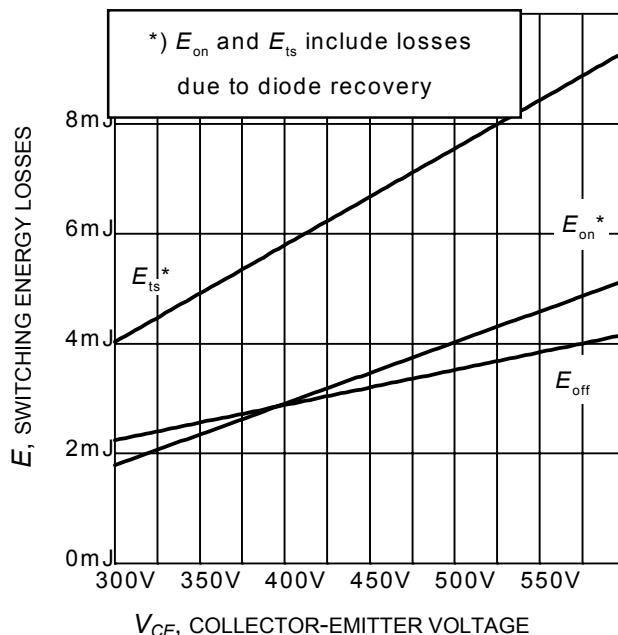


Figure 16. Typical switching energy losses as a function of collector-emitter voltage
(inductive load, $T_J = 175^\circ\text{C}$,
 $V_{GE} = 0/15\text{V}$, $I_C = 75\text{A}$, $R_G = 5\Omega$,
Dynamic test circuit in Figure E)

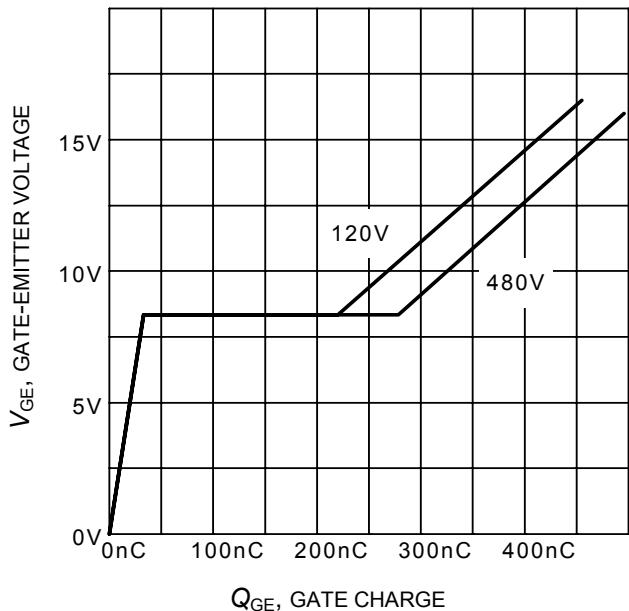


Figure 17. Typical gate charge
($I_C=75$ A)

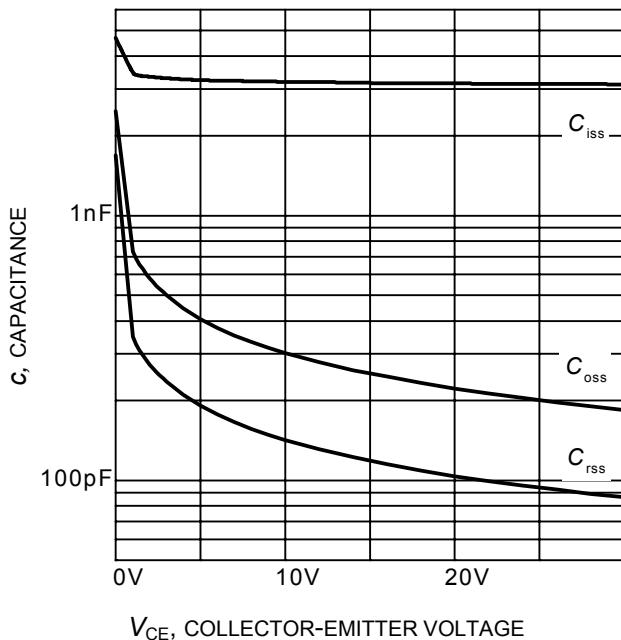


Figure 18. Typical capacitance as a function of collector-emitter voltage
($V_{GE}=0$ V, $f = 1$ MHz)

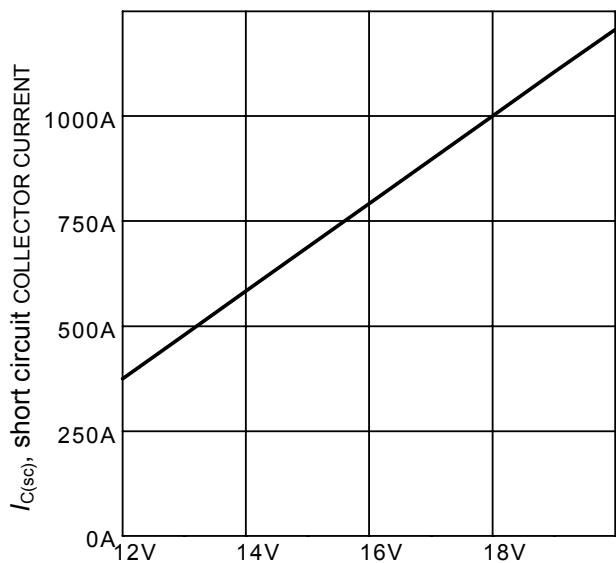


Figure 19. Typical short circuit collector current as a function of gate-emitter voltage
($V_{CE} \leq 400$ V, $T_j \leq 150^\circ\text{C}$)

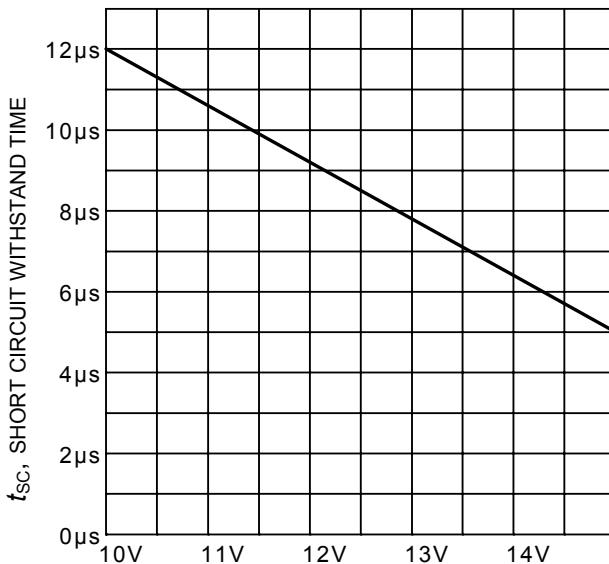


Figure 20. Short circuit withstand time as a function of gate-emitter voltage
($V_{CE}=600$ V, start at $T_j=25^\circ\text{C}$,
 $T_{jmax}<150^\circ\text{C}$)

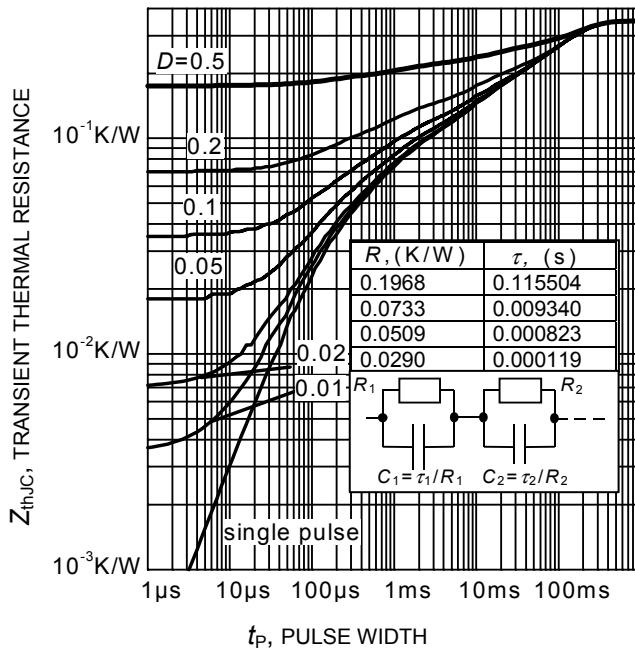


Figure 21. IGBT transient thermal resistance
 $(D = t_p / T)$

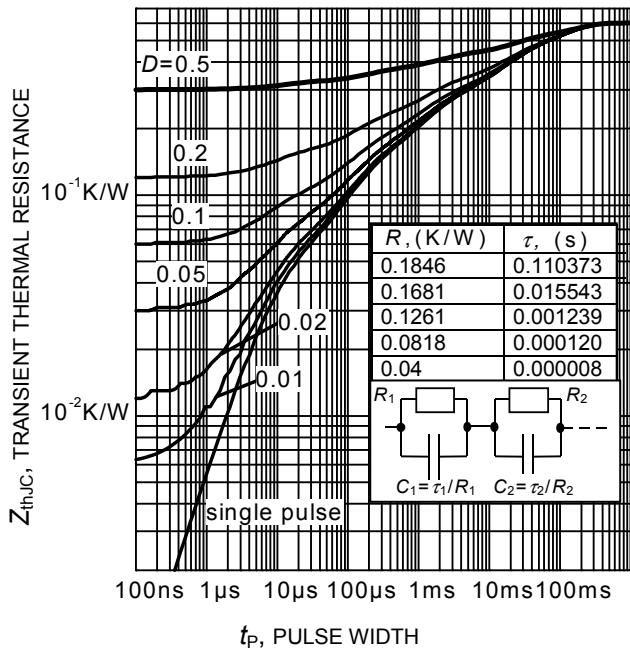


Figure 22. Diode transient thermal impedance as a function of pulse width
 $(D=t_p/T)$

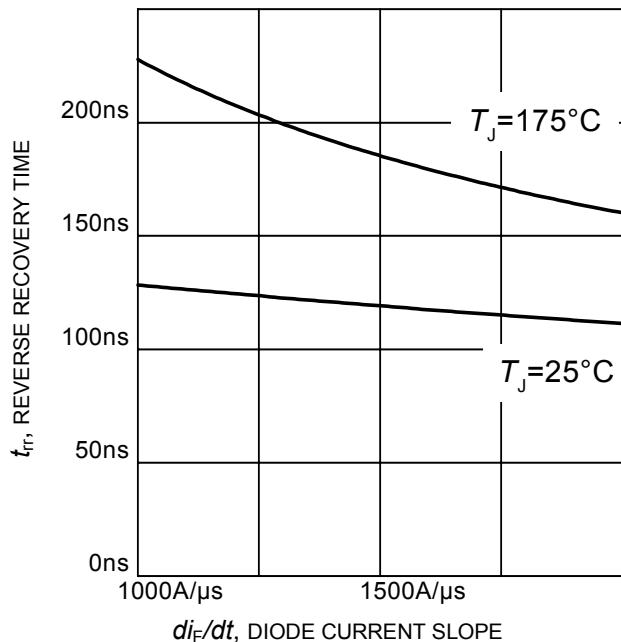


Figure 23. Typical reverse recovery time as a function of diode current slope
 $(V_R=400V, I_F=75A,$
Dynamic test circuit in Figure E)

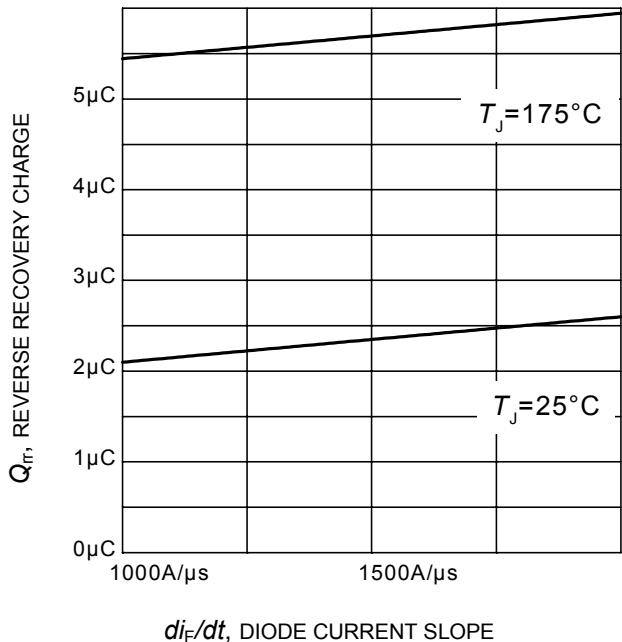
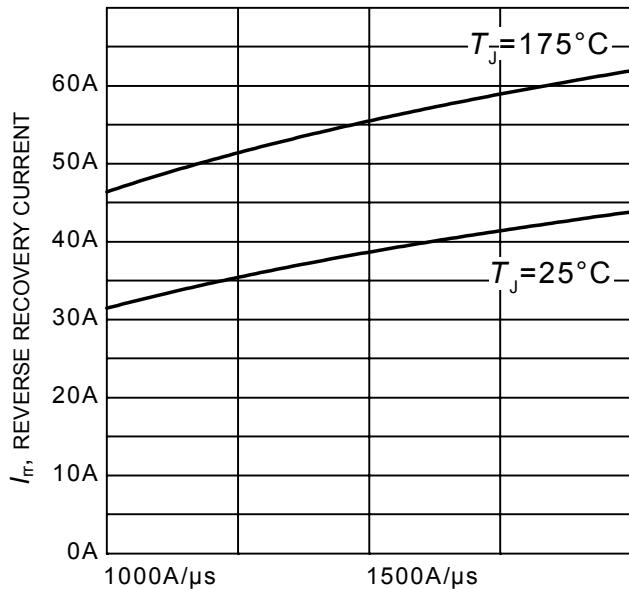


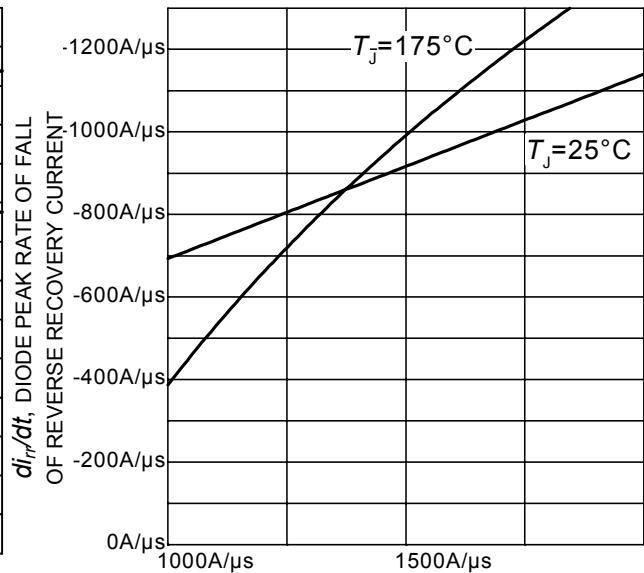
Figure 24. Typical reverse recovery charge as a function of diode current slope
 $(V_R = 400V, I_F = 75A,$
Dynamic test circuit in Figure E)



di_F/dt , DIODE CURRENT SLOPE

Figure 25. Typical reverse recovery current as a function of diode current slope

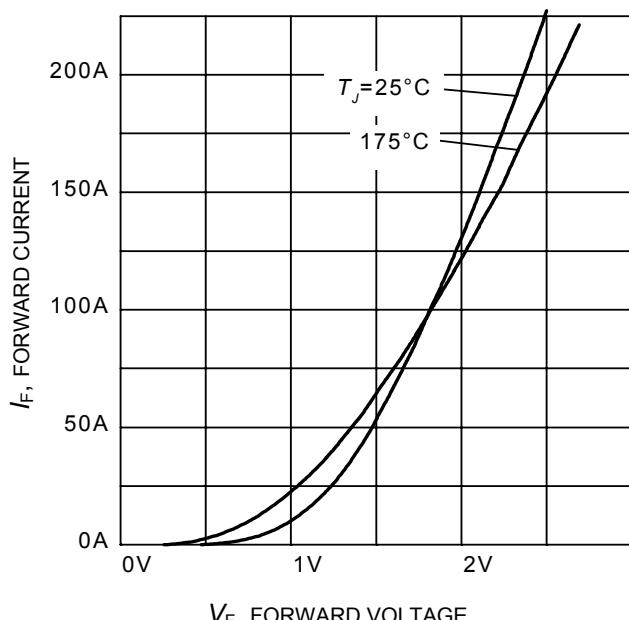
($V_R = 400\text{V}$, $I_F = 75\text{A}$,
Dynamic test circuit in Figure E)



di_F/dt , DIODE CURRENT SLOPE

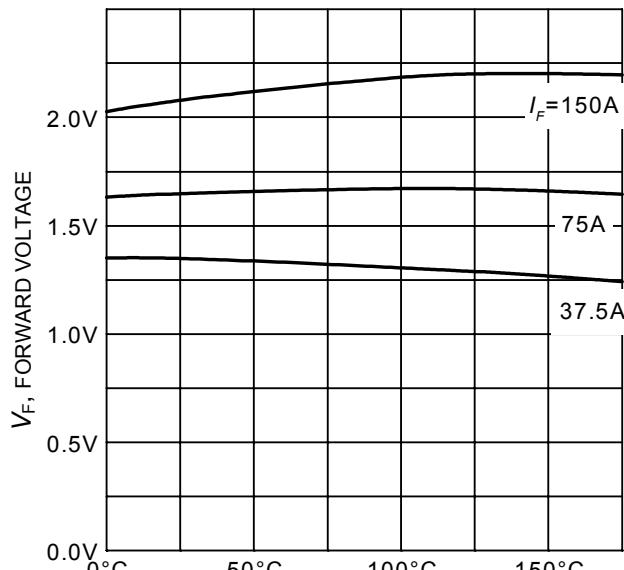
Figure 26. Typical diode peak rate of fall of reverse recovery current as a function of diode current slope

($V_R = 400\text{V}$, $I_F = 75\text{A}$,
Dynamic test circuit in Figure E)



V_F , FORWARD VOLTAGE

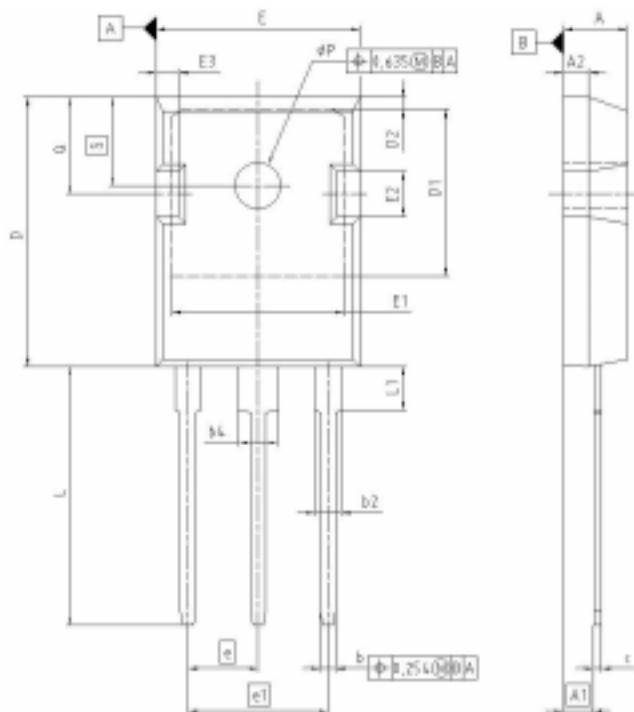
Figure 27. Typical diode forward current as a function of forward voltage



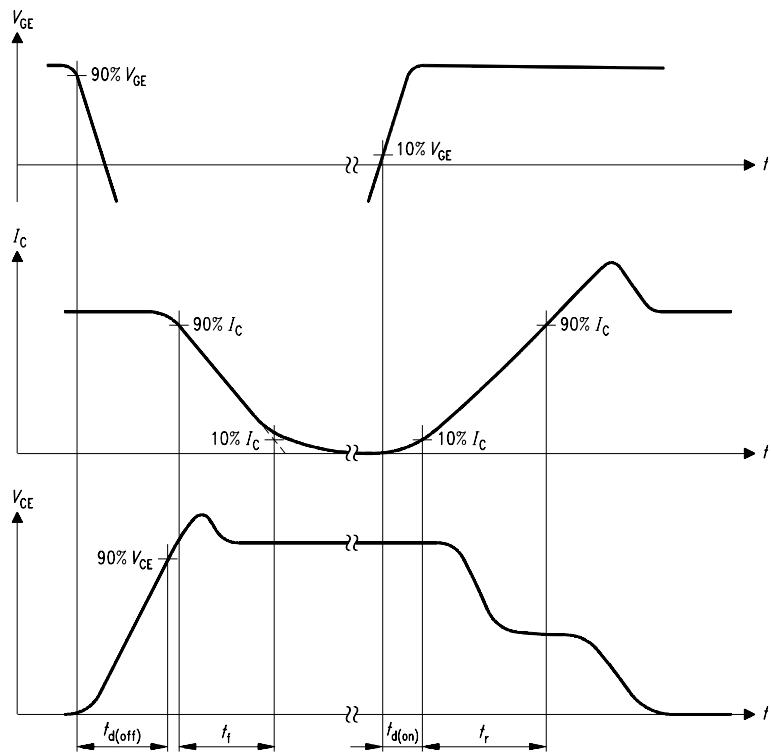
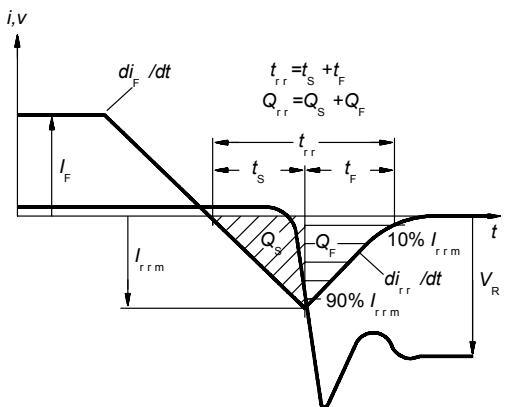
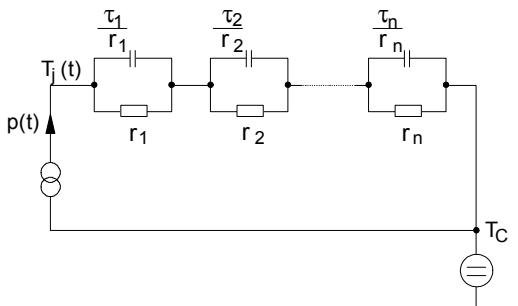
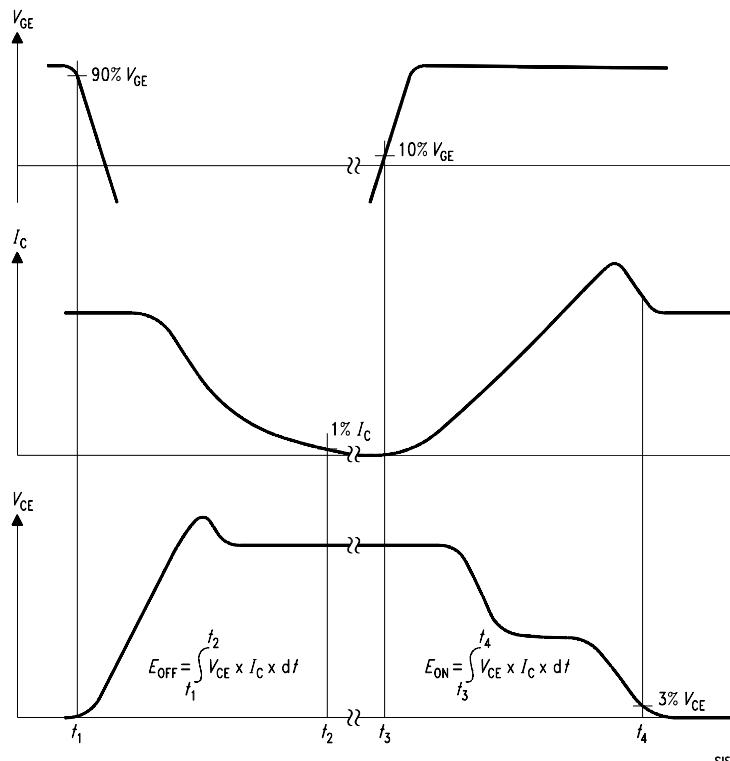
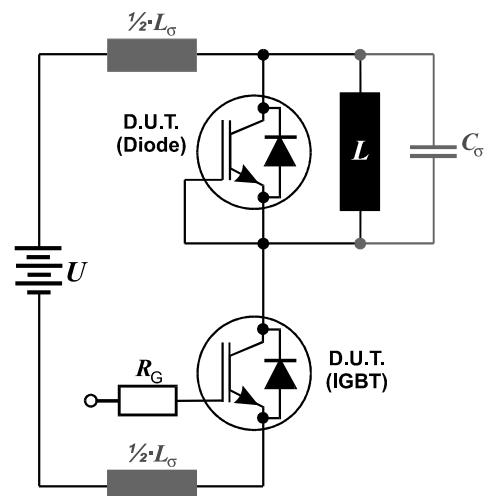
T_J , JUNCTION TEMPERATURE

Figure 28. Typical diode forward voltage as a function of junction temperature

PG-T0247-3-21



DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.903	5.157	0.193	0.209
A1	2.275	2.527	0.092	0.096
A2	1.683	2.107	0.065	0.081
b	1.073	1.327	0.042	0.052
b2	1.903	2.288	0.075	0.094
b4	2.679	3.454	0.113	0.136
c	0.549	0.752	0.022	0.030
D	20.823	21.097	0.820	0.850
D1	17.323	17.801	0.682	0.702
D2	1.063	1.337	0.042	0.052
E	15.778	16.027	0.618	0.651
E1	13.883	14.147	0.547	0.557
E2	3.583	3.987	0.145	0.158
E3	1.803	1.937	0.069	0.076
e	5.452		0.215	
e1	18.000		0.430	
M	3		3	
L	23.253	23.387	0.793	0.799
L1	4.698	4.472	0.184	0.176
φP	8.559	8.661	0.340	0.344
Q	5.403	5.747	0.218	0.226
S	8.043	8.297	0.320	0.246

TrenchStop Series

Figure A. Definition of switching times

Figure C. Definition of diodes switching characteristics

Figure D. Thermal equivalent circuit

Figure B. Definition of switching losses

Figure E. Dynamic test circuit

Edition 2006-01

Published by

**Infineon Technologies AG
81726 München, Germany**

© Infineon Technologies AG 5/8/06.

All Rights Reserved.

Attention please!

The information given in this data sheet shall in no event be regarded as a guarantee of conditions or characteristics ("Beschaffenheitsgarantie"). With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Infineon Technologies hereby disclaims any and all warranties and liabilities of any kind, including without limitation warranties of non-infringement of intellectual property rights of any third party.

Information

For further information on technology, delivery terms and conditions and prices please contact your nearest Infineon Technologies Office (www.infineon.com).

Warnings

Due to technical requirements components may contain dangerous substances. For information on the types in question please contact your nearest Infineon Technologies Office.

Infineon Technologies Components may only be used in life-support devices or systems with the express written approval of Infineon Technologies, if a failure of such components can reasonably be expected to cause the failure of that life-support device or system, or to affect the safety or effectiveness of that device or system. Life support devices or systems are intended to be implanted in the human body, or to support and/or maintain and sustain and/or protect human life. If they fail, it is reasonable to assume that the health of the user or other persons may be endangered.